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1. **Code compression for embedded VLIW processors using variable-to-fixed coding**

Yuan Xie; Wolf, W.; Lekatsas, H.;

[Very Large Scale Integration \(VLSI\) Systems, IEEE Transactions on](#)  
Volume 14, Issue 5, May 2006 Page(s):525 - 536  
Digital Object Identifier 10.1109/TVLSI.2006.876105[AbstractPlus](#) | [Full Text: PDF\(736 KB\)](#) [IEEE JNL](#)[Rights and Permissions](#)2. **A code decompression architecture for VLIW processors**

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[Microarchitecture, 2001. MICRO-34. Proceedings, 34th ACM/IEEE International Symposium on](#)  
1-5 Dec. 2001 Page(s):66 - 75

Digital Object Identifier 10.1109/MICRO.2001.991106

[AbstractPlus](#) | [Full Text: PDF\(1018 KB\)](#) [IEEE CNF](#)[Rights and Permissions](#)3. **Code compression for VLIW processors using variable-to-fixed coding**

Yuan Xie; Wolf, W.; Lekatsas, H.;

[System Synthesis, 2002. 15th International Symposium on](#)  
2002 Page(s):138 - 143[AbstractPlus](#) | [Full Text: PDF\(530 KB\)](#) [IEEE CNF](#)[Rights and Permissions](#)4. **Compiler-driven cached code compression schemes for embedded ILP processors**

Larin, S.Y.; Conte, T.M.;

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Heikkilä, J.; Cilio, A.; Takala, J.; Corporaal, H.;

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23-26 May 2005 Page(s):1122 - 1125 Vol. 2

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6. **LZW-based code compression for VLIW embedded systems**  
Chang Hong Lin; Yuan Xie; Wolf, W.;  
[Design, Automation and Test in Europe Conference and Exhibition, 2004. Proceedings](#)  
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7. **Dictionary-based program compression on TTAs: effects on area and power consumption**  
Heikkilä, J.; Takala, J.; Corporaal, H.;  
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Digital Object Identifier 10.1109/SIPS.2005.1579916  
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8. **Making compaction-based parallelization affordable**  
Nakatani, T.; Ebcioğlu, K.;  
[Parallel and Distributed Systems, IEEE Transactions on](#)  
Volume 4, Issue 9, Sept. 1993 Page(s):1014 - 1029  
Digital Object Identifier 10.1109/71.243528  
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9. **A single mediaprocessor-based programmable ultrasound system**  
Sikdar, S.; Managuli, R.; Lixin Gong; Shamdasani, V.; Mitake, T.; Hayashi, T.; Yongmin Kim;  
[Information Technology in Biomedicine, IEEE Transactions on](#)  
Volume 7, Issue 1, March 2003 Page(s):64 - 70  
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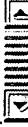
10. **Performance comparison of two-dimensional discrete wavelet transform computation schemes on a digital signal processor**  
Masselos, K.; Andreopoulos, Y.; Stouraitis, T.;  
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Volume 153, Issue 2, 6 April 2006 Page(s):173 - 180  
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11. **An instruction set architecture based code compression scheme for embedded processors**  
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### Search History

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L41	(vliw\$1 or very near1 (large or long)) near20 (compress\$5 or decompress\$5) and (mask\$5) near35 (null or nop or "no" near1 operation or mask\$5)	108
	(vliw\$1 or very near1 (large or long)) near35 (mask\$5) near35 (null or	

<u>L40</u>	nop or "no" near1 operation or mask\$5) near35 (compress\$5 or decompress\$5)	10	<u>L40</u>
<u>L39</u>	l31 and l19	2	<u>L39</u>
<u>L38</u>	5669001.pn.	2	<u>L38</u>
<u>L37</u>	l36 and l15	28	<u>L37</u>
<u>L36</u>	l32 and (length or width or size)	570	<u>L36</u>
<u>L35</u>	l32 near55 (length or width or size)	65	<u>L35</u>
<u>L34</u>	l33 not l6	65	<u>L34</u>
<u>L33</u>	l32 near35 (length or width or size)	65	<u>L33</u>
<u>L32</u>	(vliw\$1 or very near1 (large or long)) near35 (mask\$5) near35 (null or nop or "no" near1 operation or mask\$5)	680	<u>L32</u>
<u>L31</u>	(vliw or very near1 (large or long)) near35 (mask\$5) near35 (null or nop or "no" near1 operation or mask\$5)	680	<u>L31</u>

*DB=PGPB,USPT; PLUR=YES; OP=OR*

<u>L30</u>	l25 not l26	12	<u>L30</u>
<u>L29</u>	l12 and l23	1	<u>L29</u>
<u>L28</u>	l12 and l21	0	<u>L28</u>
<u>L27</u>	l12 and l19	4	<u>L27</u>
<u>L26</u>	l12 and l17	28	<u>L26</u>
<u>L25</u>	l12 and l15	40	<u>L25</u>
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<u>L23</u>	(710/66-68)![CCLS]	602	<u>L23</u>
<u>L22</u>	(717/141-143)[CCLS]	721	<u>L22</u>
<u>L21</u>	(717/141-143)[CCLS]	721	<u>L21</u>
<u>L20</u>	(717/106-167)![CCLS]	9592	<u>L20</u>
<u>L19</u>	(717/106-167)[CCLS]	9592	<u>L19</u>
<u>L18</u>	(712/24,225,227,300)[CCLS]	1872	<u>L18</u>
<u>L17</u>	(712/24,225,227,300)[CCLS]	1872	<u>L17</u>
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<u>L15</u>	(712/2-300)![CCLS]	12943	<u>L15</u>

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<u>L14</u>	l13 not l6	79	<u>L14</u>
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<u>L13</u>	L12 not l11	85	<u>L13</u>
	(vliw or very near1 (large or long)) near35 (reduc\$5 or compres\$5 or		
<u>L12</u>	compact\$4 or decompress\$5) near45 (null or nop or "no" near1 operation or mask\$5)	106	<u>L12</u>
	(vliw or very near1 (large or long)) near55 (reduc\$5 or compres\$5 or		
<u>L11</u>	compact\$4 or decompress\$5) near6 (length or width or size) near25 (null or nop or "no" near1 operation or mask\$5)	21	<u>L11</u>
	(vliw or very near1 (large or long)) and (reduc\$5 or compres\$5 or		
<u>L10</u>	compact\$4 or decompress\$5) near6 (length or width or size) near25 (null or nop or "no" near1 operation or mask\$5)	508	<u>L10</u>
<u>L9</u>	L6 not (l1 or l3)	68	<u>L9</u>
<u>L8</u>	L6 not l1	70	<u>L8</u>
<u>L7</u>	L6 not l3	68	<u>L7</u>
	(vliw or very near1 (large or long)) and (compres\$5 or compact\$4 or		
<u>L6</u>	decompress\$5) near6 (length or width or size) near25 (null or nop or "no" near1 operation or mask\$5)	77	<u>L6</u>
	(vliw or very near1 (large or long)) and (compres\$5 or compact\$4 or		
<u>L5</u>	decompress\$5) near6 (length or width or size) near45 (null or nop or "no" near1 operation or mask\$5)	77	<u>L5</u>
<u>L4</u>	L3 not l1	3	<u>L4</u>
	(vliw or very near1 (large or long)) near45 (compres\$5 or compact\$4		
<u>L3</u>	or decompress\$5) near6 (length or width or size) near45 (null or nop or "no" near1 operation or mask\$5)	10	<u>L3</u>
<u>L2</u>	6704859.pn.	3	<u>L2</u>
	(vliw or very near1 (large or long)) near25 (compres\$5 or compact\$4		
<u>L1</u>	or decompress\$5) near4 (length or width or size) near25 (null or nop or "no" near1 operation)	7	<u>L1</u>

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